

IN THE CLAIMS:

The following is a complete listing of the claims, and replaces all earlier listings and all earlier versions.

1. (Previously Presented) An image processing device, comprising a memory unit having a memory region for storing images of at least one screen, a memory control unit for performing an input system operation to write image data to the memory unit by using a first clock and a first image synchronizing signal and for performing an output system operation to output image data read out from the memory unit by using a second clock and a second image synchronizing signal, a clock generating unit for generating the second clock, and a synchronizing control unit for inputting the second clock and for outputting the second image synchronizing signal,

wherein said synchronizing control unit generates a third image synchronizing signal asynchronous to the first image synchronizing signal by dividing the second clock and a fourth image synchronizing signal, and being synchronized to the first image synchronizing signal by using the second clock and selects one of the third image synchronizing signal and the fourth image synchronizing signal to output it as the second image synchronizing signal.

2. (Previously Presented) An image processing device according to claim 1, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signal according to a vertical

frequency of the first image synchronizing signal and outputs it as the second image synchronizing signal.

3. (Previously Presented) An image processing device according to claim 1, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signal according to a purpose for a use of the output image and outputs it as the second image synchronizing signal.

4. (Previously Presented) An image processing device, comprising a memory unit having a memory region for storing images of at least one screen, a memory control unit for performing an input system operation to write image data to the memory unit on by using a plurality of first clocks and a plurality of first image synchronizing signals synchronized to a plurality of input signals inputted to said plurality of input system signal processing units and for performing an output system operation to output image data read out from the memory unit by using a second clock and a second image synchronizing signal, a clock generating unit for generating the second clock, and a synchronizing control unit for inputting the second clock and for outputting the second image synchronizing signal,

wherein said synchronizing control unit generates a third image synchronizing signal asynchronous to the first image synchronizing signals by dividing the second clock and fourth image synchronizing signals, and being synchronized to the first image synchronizing signals by using the second clock and selects one of the third image

synchronizing signal and the fourth image synchronizing signals to output it as the second image synchronizing signal.

5. (Previously Presented) An image processing device according to claim 4, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signals according to a vertical frequency of the first image synchronizing signals and outputs it as the second image synchronizing signal.

6. (Previously Presented) An image processing device according to claim 4, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signals according to a purpose for a use of the output image and outputs it as the second image synchronizing signal.

7. (Previously Presented) An image processing device according to claim 4, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signals according to presence or absence of a dynamic image or a proportion thereof in the plurality of input signals inputted to said plurality of input system signal processing units and outputs it as the second image synchronizing signal.

8. (Previously Presented) An image processing device according to claim 4, wherein said synchronizing control unit selects one of the third image synchronizing signal and the fourth image synchronizing signals according to uses or types of the plurality of input system signals and outputs it as the second image synchronizing signal.

9. (Previously Presented) An image processing device according to claim 4, further comprising means for outputting a request of setting or re-setting input image signals to signal sources for inputting signals to said plurality of input system signal processing units and for outputting a request of a synchronization to the second image synchronizing signal to an arbitrary input signal source which is asynchronous to the second image synchronizing signal selected to be output from the third image synchronizing signal and the fourth image synchronizing signals.

10. - 32. (Canceled).

33. (Previously Presented) An image processing device according to claim 1, wherein the image processing device is used as a signal processing unit of an image display device.

34. (Previously Presented) An image processing device according to claim 1, wherein the image processing device is used as a signal processing unit for an image display unit of a computer.

35. (Previously Presented) An image processing device according to claim 1, wherein the image processing device is used as a signal processing unit for an image display unit of a digital TV.

36. (Previously Presented) An image processing device according to claim 33, wherein said image display device has a liquid crystal display unit.

37. (Previously Presented) An image processing device according to claim 33, wherein said image display device has a display unit of a plasma display or an electric-charge emission type device.

38. (Previously Presented) An image processing device according to claim 33, wherein said image display device has a display unit of a reflection type device which displays an image by reflecting light.

39. (Previously Presented) A computer readable medium on which a program is recorded for a computer to execute the operations of the image processing device according to claim 1.